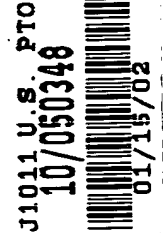


Inventor: Gurtej S. Sandhu et al.

Title: Transistor Structures

Assignee: Micron Technology, Inc.

Attorney Docket No. MI22-1898



**INFORMATION DISCLOSURE STATEMENT**

**PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98**

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a divisional application of co-pending application Serial No. 09/633,556, filed August 7, 2000. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. § 1.98(d) and MPEP § 609(2).

Citation of these references is respectfully requested.

Dated: \_\_\_\_\_

1/15/02

Respectfully submitted,

A handwritten signature of David G. Latwesen is written over a horizontal line.

David G. Latwesen, Ph.D.  
Reg. No. 38,533